

										20190223		20190307(Changed Thermal Pad and T- Flex 700 series (K-5 Wink))				20190223		20190307(Changed Thermal Pad and T- Flex 700 series (K-5 Wink))			
Symbol	Description	Power Dissipation(W)	Component Specification				Simulation Results				20190223		20190307		Pass	Pass	Pass	Pass			
			R _p (°C/W)	T _{pp} (°C/W)	R _{ps} (°C/W)	T _a Spec. (°C)	T _c Spec. (°C)	T _p Spec. (°C)	T _p (°C)	T _c (°C)	T _p (°C)	T _c (°C)									
U001	TLV320A01R100V1 CPU	0.191	51	27	146.3	100	82.1	92.9	50.2	59.941	49.8	59.941	50.541	49.8	PASS	PASS	PASS	PASS			
U002	TLV320A01R100V1 CPU & cam	0.191	51	27	146.3	100	82.4	92.1	49	59.741	49.8	59.741	50.541	49.8	PASS	PASS	PASS	PASS			
U003	TLV320A01R100V1 System	0.191	51	27	146.3	100	82.4	92.1	47	59.741	47	59.741	50.541	47	PASS	PASS	PASS	PASS			
U004	TLV320A01R100V1 CPU	0.191	51	27	146.3	100	79.1	88.8	47.2	55.941	46.2	55.941	46.2	55.941	PASS	PASS	PASS	PASS			
U005	FPF1081000100V1 CPU	0.191	100	250	85	100	83.1	105.1	46.5	73.93	46.1	72.93	72.93	46.1	PASS	PASS	PASS	PASS			
U006	FPF1081000100V1 System	0.191	100	250	85	100	83.1	105.1	53.2	78.93	52.8	77.93	77.93	52.8	PASS	PASS	PASS	PASS			
U007	FPF1081000100V1 CPU	0.191	100	250	70	100	81.4	105.2	46.4	71.23	46	70.93	70.93	46	PASS	PASS	PASS	PASS			
U1	SPR002-2 CPU	0.9921	6.36	10.7	18.4	70	100	75.3	78.4	51.9	54.42058	48.3	51.42058	48.3	PASS	PASS	PASS	PASS			
U008	HEA808000R100V1 XNG	0.1870	9.8	27.4	82.9	95	82.5	83.9	49	50.8326	50.2	50.8326	50.2	50.8326	PASS	PASS	PASS	PASS			
U009	HEA808000R100V1 XNG	0.1870	9.8	27.4	82.9	95	85.0	86.9	50	51.8326	50.9	52.7826	50.9	52.7826	PASS	PASS	PASS	PASS			
U010	HEA808000R100V1 XNG	0.1870	9.8	27.4	82.9	95	85.0	87.4	51.5	52.8326	51.2	53.6826	51.2	53.6826	PASS	PASS	PASS	PASS			
U011	HEA808000R100V1 XNG	0.1870	9.8	27.4	82.9	95	82.0	83.9	49	50.8326	50.4	52.2326	50.4	52.2326	PASS	PASS	PASS	PASS			
U1001	20MHz	0.0002	2	4.5	36.7	85	74.5	92.6	45.5	49.0904	45.3	46.9904	45.3	46.9904	PASS	PASS	PASS	PASS			
U1001	RT1801000100V1 CPU	0.0438	51	27	146.3	100	74.7	74.7	49.5	49.5	49.1	49.1	49.1	49.1	PASS	PASS	PASS	PASS			
U1001	RT1801000100V1 CPU	0.0438	51	27	146.3	100	73.9	73.9	49.5	49.5	49.1	49.1	49.1	49.1	PASS	PASS	PASS	PASS			
U1001	24MHz	0.0438	51	27	146.3	100	77.7	77.7	50.3	50.3	49.9	49.9	49.9	49.9	PASS	PASS	PASS	PASS			
U1001	24MHz	0.0438	51	27	146.3	100	72.7	72.7	50.3	50.3	49.9	49.9	49.9	49.9	PASS	PASS	PASS	PASS			
U11001	TLV320A01R100V1 CPU	0.1000	0.4	34.1	11.5	33.9	85	100	71.9	72.0	43.8	47.21	43.4	46.61	PASS	PASS	PASS	PASS			
Top Housing	Housing														13.0	13.0					
Top Housing	Housing																				
Top Housing	Outside																				
Bottom	Housing																				
Bottom	Housing																				
Bottom	Inside																				
Bottom	Housing																				
Bottom	Outside																				
AP																					
Total		2.9475																			

